Mm-wave Antenna-System Designs dedicated to high-data rate communications

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The future needs of the global mobile data traffic <u>induces</u> the massive arrival of new connected devices



Consequences

The global mobile data traffic will rise to 600 exabyte/month in 2025

Challenge

Develop wireless links targeting high data rates communications

Years



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To address this data challenge a progressive 5G roll out is being planned by telecom industries since the beginning of 2020



In order to support the 5G network and applications roll out, our research work, since 2010, has been concentrated on two main applications:

- Communications between small devices over few meters for kiosk downloading or docking stations wireless links
- \rightarrow WiGig (60 GHz), D-band (120 GHz) and Sub-THz (200-300 GHz)
- Backhaul and fronthaul communications
- → Mm-wave and THz point-to-point wireless links targeting 40 Gbit/s



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 Since data consumption is going higher and higher, short distance (< 3m) high speed wireless solution is a key differentiator (cable replacement):

Short distance ad hoc link:

Cloud based Sync. & delivery services:



http://www.theverge.com/2013/1/14/3875308/wlgig-gets-officialstandards-for-short-range-high-speed-wireless





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Challenge of Wireless backhaul / fronthaul networks



- Densification of the network (Smalls cells x 10) implies a strong pressure on backhaul links.
- 2 solutions:
 - Optics fibers installation is too expensive and cannot be set everywhere.
 - Point-to-point mmW wireless link is the most attractive solution.
- R&D at D Band (110 140 GHz): wide bandwidth (> 20 GHz) \rightarrow 20 Gbit/s
- Sub-THz frequencies could enable wider bandwidth & higher data rates \rightarrow 40 Gbit/s

- 60/120GHz High Gain Antenna Specifications
- Since the output power level is limited, the transmission range of the system mainly depends on the *gain of the antenna*.



Our research work investigated silicon-based technical solutions

Several chipset solutions demonstrated the possibility to use silicon technology to address mm-Wave high-data rate transmissions

IBM / Mediatek	ST	Stanford	Berkeley			
Pront-Ends Print-End Print-P			V-band D/F-band	D/F-band	J-band	
			57-66 GHz	120-140 GHz	200-280 GHz	
				15%	15%	34%
			1-5 Gbps	10 Gbps	40 Gbps	

The main challenge concerns an efficient Circuit/Antenna combination

- Low-loss mmW packaging technology
- Low-cost mmW packaging technology
 - Assembly strategy compliant with industrial constraints

• For example, the availability of cost-effective silicon mmW chipsets will not be enough in order to reduce the cost of backhaul / fronthaul links



 So, low-cost high-gain mmW antenna solution is a key enabler in order to support the development of cost effective backhaul/fronthaul links that can leverage the integration capability and cost effectiveness of silicon technologies.

Outline

SoC or SiP IC/Antenna integration scheme?

Low-Gain Antennas in organic packaging technology

Antenna integration strategies: current status

High-Gain Antennas in organic packaging & 3D printing technologies

10 Gb/s Low-energy point-to-point demo at 120 GHz

Perspectives

Conclusion

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SoC or SiP IC/Antenna integration scheme?

Integrating 60 GHz mm-wave antennas on chip was the first idea but ...

... antennas integrated on standard or high-end Si CMOS process (SoC approach) exhibited poor gain, clearly not in line with WiGig transmission over few meters (above 5 dBi)

	Reference	Process	Antenna Type	Gain (dBi)	Size	
ow-Resistivity Silicon	[2]	Silicon	IFA	-19	2 mm long	
	[2]	Silicon	Quasi Yagi	-12.5	1.3 mm long	
	[3]	0.18-µm CMOS	Yagi	-10.6	1.1 x 0.95 mm²	
	[4]	0.18-µm CMOS	IFA	-15.6	0.28 x 0.27 mm ²	[2] Zhang et al., 2005
	[5]	0.18-µm CMOS	Triangular Monopole	-9.4	1 x 0.81 mm²	[3] Hsu et al., 2008
	ST/UNS	0.13-µm CMOS	IFA	-2.7	1 x 1 mm²	[5] Lin et al. 2007
	ST/UNS	0.13-µm CMOS	Dipole	-7.9	1 x1 mm²	[6] Chen et al., 2009 [7] Bao et al., 2012
igh-Resistivity Silicon	[6]	0.15-µm pHEMT	Dipole	3.6	0.9 mm²	[8] Barakat et al., 2011
	[7]	0.18-µm CMOS	Loop with AMC	4.4	1.8 x 1.8 mm ²	[9] Barakat et al., 2010
	[8]	0.13-µm HR SOI	Double Slot	-5.5	1.2 mm long	
	[9]	0.13-µm HR SOI	Interdigitated Dipole	3	NA	
	ST/UNS	HR SOI	Folded Slot	3.9	0.8 x 1.7 mm ²	

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SoC or SiP IC/Antenna integration scheme ?

System-in-Package approach naturally emerged as the solution

Example of a 60 GHz Antenna-in-Package

Low profile packageMulti-layer build-up

Aperture coupled-Patch Antenna

Multilayer substrate with built-in antenna



SiP or SoC integration scheme ?

Mainstream technologies for SiP were glass and multi-layer co-fired ceramics substrates (Low or High Temperature)

Tokyo Institute of Tech





I2R - LTCC

IBM Glass Substrate





IBM Laminated packages

- We had to *identify* the appropriate *packaging technology* to support the development of 60 GHz modules as possible commercial products
- We deeply investigated:
 - HTCC/LTCC
 - Organic Ball-Grid-Array module

Technology	Design Rules (min width/min space)	Cost
HTCC/LTCC	~100 µm/~100 µm	-
Organic BGA	~35 µm/~35 µm	+





The start of our collaboration with ST was a very good example of *cross-cultural development with the microelectronics world* leveraging both *antenna* & *circuit* communities' *expertise*



- Several modules from *different suppliers* were fabricated and measured showing successful reproducibility tests
- Antenna performance were fully in line with the 5 dBi Gain requirement



Organic BGA technology seems to be the best compromise between cost/performance/assembly



Total Realized Gains (dBi) – 3D plots



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Fig. 8 Low cost 60GHz WiGIG board with Digital Baseband

16 QAM – OFDM → 3.8 Gbps over 1m

SNR around 15 dB

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- X

10:[Spectrum]

Fig. 9 Measured constellations after demodulation in the HRP2 mode (OFDM-16QAM modulation) with Agilent tools.

Zoomed False

120 GHz organic module for high-data rate links: Single Element



120 GHz organic module for high-data rate links: 2x2 Array of patches



240 GHz organic module for high-data rate links: 1x2 Array



240 GHz organic module for high-data rate links: 1x2 Array





240 GHz organic module for high-data rate links: 1x2 Array

Manufactured design \neq Simulated design \rightarrow We reached the limitations of this technology in terms of drawing rules at 300 GHz



Antenna Integration Strategies:current status

- Today, two industrial integration schemes can be considered for the antenna solution:
 - Antenna-in-Package: targeting high integration level and high performance
 - Antenna-on-board: in order to enable antenna customization according to the use-case of the customer → HDI PCB or even recently FR4 PCB



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Antenna Integration Strategies:current status

FR4 Standard PCB Technology

• Design rules : 60 (line width) \times 75 (space between lines) μ m²



https://www.st.com/en/wireless-transceivers-mcus-and-modules/6o-ghz-short-range-rf-transceivers.html

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https://blog.st.com/st6o/amp/

The path towards high-gain antennas for backhaul and fronthaul wireless links



What *about* using *3D-printing technology* to fabricate plastic lenses instead of costly Teflon approach ?

	Stereolithography	Selective Laser Sintering	Fused Deposition Modeling
Cost	Expensive	Expensive	Inexpensive
Principal	Solidifies liquid resin	Sinters powdered material	Molten plastic deposition
Surface finishing	smooth	Slightly granular	Rough (dented)
Z axis resolution	Up to 5 µm	Up to 20 µm	Up to 100 µm
XY plane resolution	Up to 30 µm	Up to 300 µm	Up to 200 µm

Plastic lens fabricated in <u>Fused Deposition Modeling (FDM)</u>:

 \Box Standard ABS plastic ($\epsilon_r = ???$)

 ε_r and tan δ of the ABS-M30 at 60GHz, 120, 240 GHz?





	IST/IT	e esa	Our meas.	Teflon
Method	Fabry-Perot Open resonator	Quasi-optical meas. setup	Waveguide method	NA
Freq.	60GHz	137.5GHz	110-125GHz	NA
Er	2.48	2.48	2.49	2
$\tan \delta$	0.009	0.008	0.01	0.0002

In order to obtain the best performance, a co-design of the source with the lens is mandatory



In order to lower the *dielectric losses*, we designed a *chopped lens* Fast optimization using ILASH software tool (GO/PO) + HFSS full-wave verification



40 mm diameter lens for high-data rate links @ 120 GHz



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40 mm diameter lens for high-data rate links @ 120 GHz





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10Gb/s Low-energy Point-to-Point demo @ 120GHz

Fully-packaged low-cost energy-efficient OOK Tx/Rx device



[13] N. Dolatsha et al., "Compact 130GHz Fully Packaged Point-to-Point Wireless System with 3D-Printed 26dBi Lens Antenna Achieving 12.5Gb/s at 1.55pJ/b/m", IEEE International Solid-State Circuits Conference (ISSCC 2017), February 5-9 2017, San Francisco, USA.

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10Gb/s Low-energy Point-to-Point demo @ 120GHz



7.76 pJ/b Energy Efficiency 1.55 pJ/b/m, >40X better than state-of-the-art

• 3D Printed Plastic vs. Teflon Lenses

	Teflon Lens	3D Printed Lens
		BGA Support
Manufacturing time	~1 day	~9 hours
Manufacturing cost/complexity	High	Low
Material cost	High	Low
Lens diameter	25 mm	40 mm

120 GHz 3D-printed Cassegrain reflector with plastic casing + BGA source





120 GHz 3D-printed Cassegrain reflector with plastic casing + BGA source



120 GHz 3D-printed Cassegrain reflector with plastic casing + BGA source



300 GHz 50 dBi 3D-printed Cassegrain reflector + BGA source



Gain: 30-50 dBi For 1 to 200 m link

Design of a 50 dBi Cassegrain at 300 GHz









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Conclusion

- Enabling cost-effective wireless solutions is a key point in order to address future wireless challenges
- Silicon-based technology as CMOS/BiCMOS are suitable even beyond 200 GHz
- Cost-effective packaging with clever IC/antenna integration will continue to be a strong issue
 - → Organic laminate technology has proven its suitability up to 300 GHz
- From R&D point of view, moving to higher than 300 GHz will require aggressive design rules exceeding today's capability of organic substrate technology
- → Some innovation will be needed here

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Questions ... and maybe Answers

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